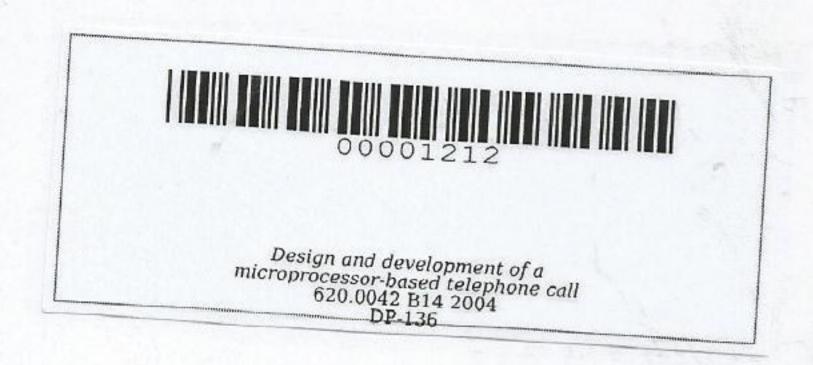
DESIGN AND DEVELOPMENT OF A MICROPROCESSOR-BASED TELEPHONE CALL MONITORING DEVICE

Undergraduate Design Project
Submitted to the Faculty of the
Cavite State University
Indang, Cavite

In partial fulfillment of the requirements for the degree of Bachelor of Science in Computer Engineering



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ABSTRACT

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The design of the Microprocessor-based Telephone Call Monitoring Device was constructed and fabricated at the second floor, Room 202, of the New Engineering Building, Cavite State University. It was tested and evaluated at the Department of Interior and Local Government Office, Indang Municipal Building.

The MC6802 microprocessor was used, which simplified the circuit design. The project was constructed to monitor the outgoing call on landline telephone sets. Monitoring involves recording of information on the calls made. This information includes the number dialed, time and date, and duration of the call. All these were saved in an eight-kilobyte EEPROM, which could store approximately 250 entries. A separate clock module was utilized which composed mainly of a BCD binary counter to produce an exact one second count for accurate timing. Connecting a printer enabled the device to produce a print out of the saved data.

The proponents developed an assembly language program as the control software, which managed the overall performance of the device. The software was embedded on a four-kilobyte EPROM. The project underwent a series of test, which revealed that the designed device was able to perform the monitoring of the calls except that it failed to meet some expected functions due to hardware complexity.

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